

TPM EtherCAT Slave : 207-A121D
RSI-ECAT-Master Operation evaluation report

1. Evaluation result

We have verified the evaluation of TPM company's 207-A121D slaves on the RSI-ECAT and judged it will operate successfully.

2. Evaluation item list and result table

Result	Evaluation contents and report	Success : Failure :																				
	<p>< Evaluation item 1 > Detection and setting of slave by configuration tool</p> <table border="1"> <thead> <tr> <th style="text-align: center;">Result</th> <th style="text-align: center;">Evaluation contents</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;"></td> <td>Presence of ESI file (Whether the ESI file is provided by the vendor.)</td> </tr> <tr> <td style="text-align: center;"></td> <td>ESI file 'None' Configuration (Whether configuration can be performed without ESI file.)</td> </tr> <tr> <td style="text-align: center;"></td> <td>ESI file 'Use' Configuration (Whether configuration can be performed using ESI file)</td> </tr> </tbody> </table> <p>We judged that it is operable because it was functioning normally with all the evaluation items.</p>	Result	Evaluation contents		Presence of ESI file (Whether the ESI file is provided by the vendor.)		ESI file 'None' Configuration (Whether configuration can be performed without ESI file.)		ESI file 'Use' Configuration (Whether configuration can be performed using ESI file)													
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✓	< Evaluation item 6 > Evaluation of digital input / output operation	
	Result	Evaluation contents
	✓	Confirmation of digital output operation by Process Image operation
	✓	Confirmation of digital input operation by Process Image operation
We judged that it is operable because it was functioning normally with all the evaluation items.		
✓	< Evaluation item 7 > Evaluation of analog input / output operation	
	Result	Evaluation contents
	✓	Confirmation of analog output operation by Process Image operation
	✓	Confirmation of analog input operation by Process Image operation
We judged that it is operable because it was functioning normally with all the evaluation items. Since initialization of the analog input and output of this slave is necessary, after accessing the Object Dictionary and preparing for analog input / output, I/O to Process Image was confirmed.		

3. Hardware information

3.1. PC information used for evaluation

CPU	Intel Core i 5-4460 3.20 GMHz
Memory	8 GByte / INtime 256 MByte
HDD	1 TByte
Network	Intel Ethernet Connection I218-V
Connection cable	LAN cable (Cat. 5e)

3.2. Slave information to be evaluated

Slave type name	Slave information		
207-A121D	Supported: ✓ Unsupported: =		
	Vendor name	TPM	
	VendorID	1707 (0x06AB)	
	ProductCode	1091703872 (0x41121440)	
	RevisionNo	16908802 (0x1020202)	
	Hardware version	1.2	
	Software version	5.11.2.2	
	Slave Type	DI/DO, AD/DA Composite slave	
	Mailbox support	EoE	=
		CoE	✓
		FoE	=
		SoE	=
		VoE	=
	Synchronization mode support	FreeRun	✓
SM-Sync		✓	
DC		✓	
Cyclic command: LRW support		✓	

	Digital	Total Input channel	8
		Total Output channel	8
	Analog	Total A/D channel	8
		Input type	Voltage or Current (Can be set for each channel)
		Input range (voltage)	-10V ~ +10V
		Input range (current)	0mA ~ 20mA
		Total D/A channel	2
		Output type	Voltage
		Output range (voltage)	-11V ~ +11V (12bit, -2048 ~ +2047 or 0~4095)
207-ADIO-KIT	The base is the same as 207-A121D. Test volume and switch, type with display		

4. Software information

OS	Microsoft Windows 10 Home
RT-OS	INtime 6.1.16250.1
Master	RSI-ECAT-Master/DC 2.92
Configurator	RSI-ECAT-Studio 1.12.210.0

5. Process image information

5.1. Process image input information (1 unit of 207-A121D)

Name	PI Offset(Bit)	Bit Size	Data Type
Slave 1 (EZE_A121D).Digital_Inputs_process_data_mapping.DI	0x0	0x8	BIT Array 8
Slave 1 (EZE_A121D).AI_process_data_mapping.AI_0	0x8	0x10	INT
Slave 1 (EZE_A121D).AI_process_data_mapping.AI_1	0x18	0x10	INT
Slave 1 (EZE_A121D).AI_process_data_mapping.AI_2	0x28	0x10	INT
Slave 1 (EZE_A121D).AI_process_data_mapping.AI_3	0x38	0x10	INT
Slave 1 (EZE_A121D).AI_process_data_mapping.AI_4	0x48	0x10	INT
Slave 1 (EZE_A121D).AI_process_data_mapping.AI_5	0x58	0x10	INT
Slave 1 (EZE_A121D).AI_process_data_mapping.AI_6	0x68	0x10	INT
Slave 1 (EZE_A121D).AI_process_data_mapping.AI_7	0x78	0x10	INT
Slave 1 (EZE_A121D).DAC_0_Monitor_process_data_mapping.MainLoopState	0x88	0x8	USINT
Slave 1 (EZE_A121D).DAC_0_Monitor_process_data_mapping.ModeState	0x90	0x8	USINT
Slave 1 (EZE_A121D).DAC_0_Monitor_process_data_mapping.DacStatus	0x98	0x10	INT
Slave 1 (EZE_A121D).DAC_0_Monitor_process_data_mapping._Alignment_4	0xA8	0x8	BIT Array
Slave 1 (EZE_A121D).DAC_1_Monitor_process_data_mapping.MainLoopState	0xB0	0x8	USINT
Slave 1 (EZE_A121D).DAC_1_Monitor_process_data_mapping.ModeState	0xB8	0x8	USINT
Slave 1 (EZE_A121D).DAC_1_Monitor_process_data_mapping.DacStatus	0xC0	0x10	INT
Slave 1 (EZE_A121D).DAC_1_Monitor_process_data_mapping._Alignment_4	0xD0	0x8	BIT Array

5.2. Process image output information (1 unit of 207-A121D)

Name	PI Offset(Bit)	Bit Size	Data Type
Slave 1 (EZE_A121D).Digital_Outputs_process_data_mapping.DO	0x0	0x8	BIT Array 8
Slave 1 (EZE_A121D).AO_process_data_mapping.AO_0	0x8	0x10	INT
Slave 1 (EZE_A121D).AO_process_data_mapping.AO_1	0x18	0x10	INT